

Notice of References Cited	Application/Control No.	Applicant(s)/Patent Under Reexamination	
	10/551,571	BOLCATO ET AL.	
	Examiner	Art Unit	Page 1 of 1
	Cuong V. Luu	2128	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-4,791,593 A	12-1988	Hennion, Bernard	703/14
*	B	US-6,075,480 A	06-2000	Deliberis, Jr., Romeo A.	342/169
*	C	US-6,181,754 B1	01-2001	Chen, Jesse E.	375/350
*	D	US-6,349,272 B1	02-2002	Phillips, Joel	703/2
*	E	US-6,504,885 B1	01-2003	Chen, Jesse E.	375/350
*	F	US-2003/0135354 A1	07-2003	Gabele et al.	703/13
*	G	US-2003/0191620 A1	10-2003	Williams et al.	703/17
*	H	US-2003/0191869 A1	10-2003	Williams et al.	709/328
*	I	US-6,687,658 B1	02-2004	Roychowdhury, Jaijeet S.	703/2
*	J	US-6,778,025 B1	08-2004	Leuthold, Oskar	331/16
*	K	US-2006/0277018 A1	12-2006	Bolcato et al.	703/014
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Xin Li et al.□□A Frequency Relaxation Approach for Analog/RF System-Level Simulation, pp. 842-847□□2004 ACM 1-58113-828-8/04/0006
	V	Jose Luis Pino, Khalil Kalbasi□□Cosimulation Synchronous DSP Application with Analog RF Circuit, pp. 17101714□□0-7803-5148-7/98 1998 IEEE
	W	Jean Ravatin et al.□□Full Transceiver Circuit Simulation Using VJDL-AMS, pp. 29-33□□Microwave Engineering, May 2002
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.